

REMARKS

These remarks respond to the Office Action mailed January 13, 2005. Claims 10-12 have been canceled. New claim 13 has been added. Claims 1-9 and 13 are pending in the application.

Claim Rejections - 35 U.S.C. § 101

Claims 10-12 were rejected under § 101 as directed to non-statutory subject matter. These claims have been canceled.

Claim Rejections - 35 U.S.C. § 102

Claims 1-6 and 8-12 were rejected under § 102 as being anticipated by Japanese Patent JP 07-141325 to Nakazawa. Nakazawa discloses a signal processor with a number of register files and computing elements. With reference to Fig. 1, the signal processor in Nakazawa has a first data selector 30a, a second data selector 20b, and a number of input selectors 51a-54a and 51b to 54b.

Conversely, the arithmetic apparatus of amended claim 1 comprises a plurality of pairs of recording means and operating means, with selecting means inserted between a selected pair of recording means and operating means. Nakazawa discloses a number of selectors between the register files and computing elements. Nakazawa fails to disclose or suggest selecting means inserted between a predetermined pair of recording means and operating means. Thus, Nakazawa fails to disclose or suggest all of the elements of claim 1.

Claim 6 recites, among other elements, an arithmetic apparatus with selecting means that select, during an inner product operation, a plurality of recording means one by one in a round-robin fashion. The Examiner has not stated how Nakazawa anticipates this element. Thus, Nakazawa does not anticipate every element of this claim.

Similarly, claims 8 and 9 recite, among other elements, an entertainment apparatus with a selector that

selects, during an inner product operation, a plurality of registers one by one in a round-robin fashion. The Examiner has also not stated how Nakazawa anticipates this element. Thus, Nakazawa does not anticipate every element of these claims.

With regard to amended independent claim 10, Nakazawa fails to disclose or suggest a system which comprises instructions for inputting arithmetic elements during a matrix operation and during an inner product operation. Thus, Nakazawa fails to disclose or suggest all of the elements of claim 10.

Claim 10 was further rejected as anticipated by U.S. Patent No. 5,311,459 to D'Luna. D'Luna is directed to an integrated circuit that is capable of performing different digital signal processing functions. Like Nakazawa, D'Luna fails to disclose or suggest a system for inputting arithmetic elements during a matrix operation and during an inner product operation. D'Luna discloses only simultaneous matrix operations (Col. 4, lines 46-62).

D'Luna also fails to disclose or suggest inputting elements subjected to an inner product operation in one predetermined operating means as specifically recited by claim 10. Because claim 10 recites numerous elements that are not present in D'Luna, D'Luna fails to anticipate claim 10.

Dependent claims 2-5 are submitted as allowable by virtue of their dependency on allowable independent claims.

Claim Rejections - 35 U.S.C. § 103

Claim 7 was rejected as being obvious over Nakazawa. Claim 7 is submitted as allowable by virtue of its dependency on claim 6. Specifically, the Examiner has not stated how Nakazawa discloses an apparatus with selecting means that select, during an inner product operation, a plurality of recording means one by one in a round-robin fashion, or its equivalent.


As it is believed that all of the rejections set forth in the Office Action have been fully met, favorable

reconsideration and allowance are respectfully requested. If, however, for any reason the Examiner does not believe that such action can be taken at this time, it is respectfully requested that he/she telephone applicant's attorney at (908) 654-5000 in order to overcome any additional objections which he/she might have.

If there are any additional charges in connection with this requested amendment, the Examiner is authorized to charge Deposit Account No. 12-1095 therefor.

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Respectfully submitted,

By 

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